

FIG. 1

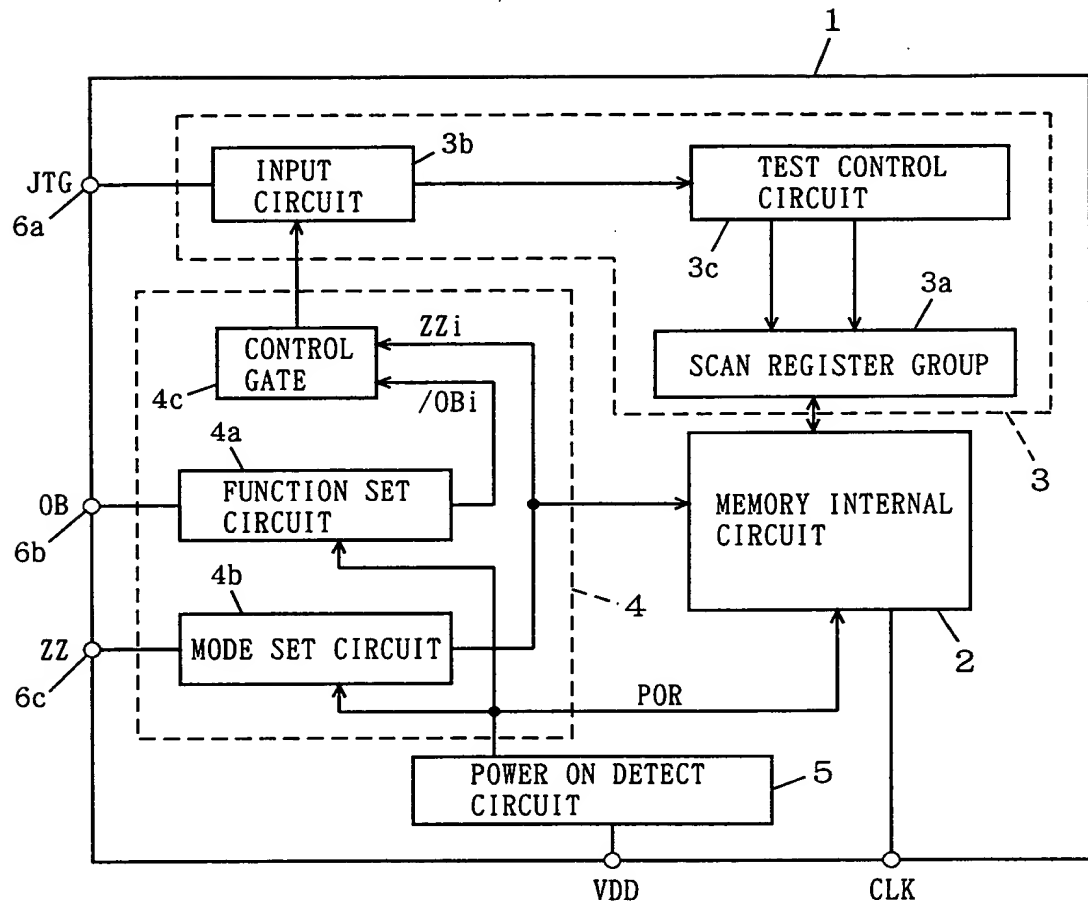


FIG. 2

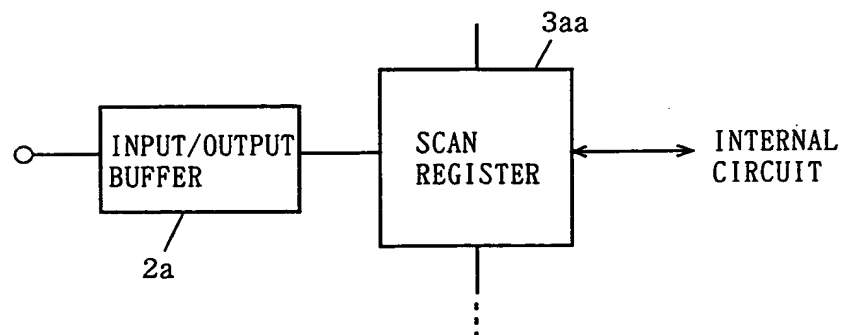


FIG. 3

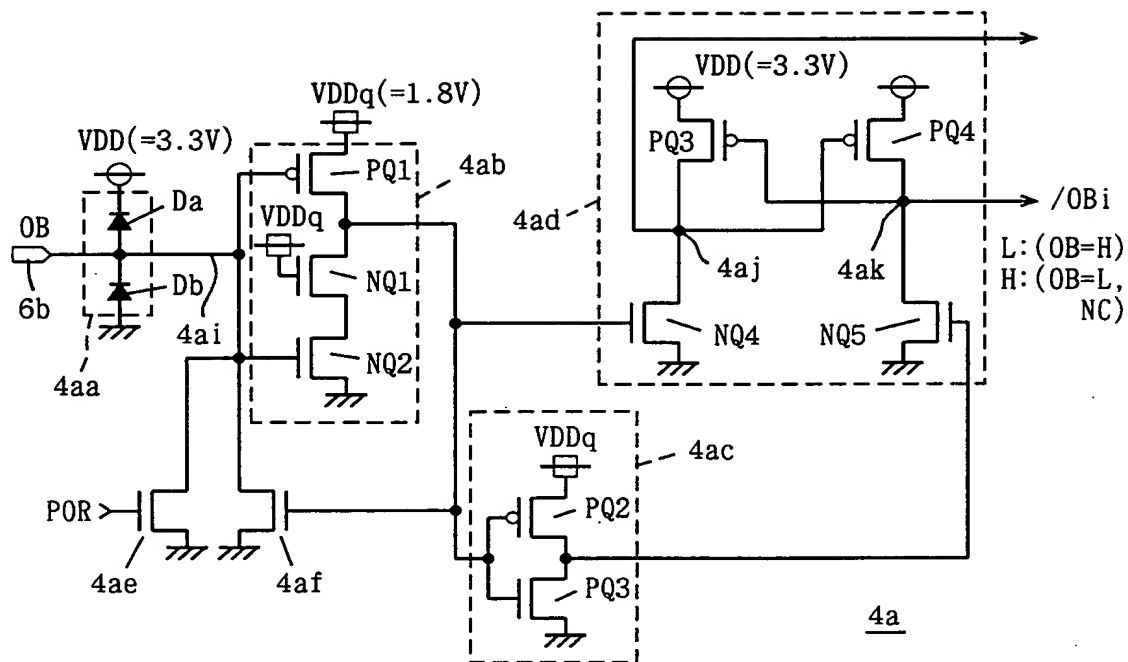


FIG. 4

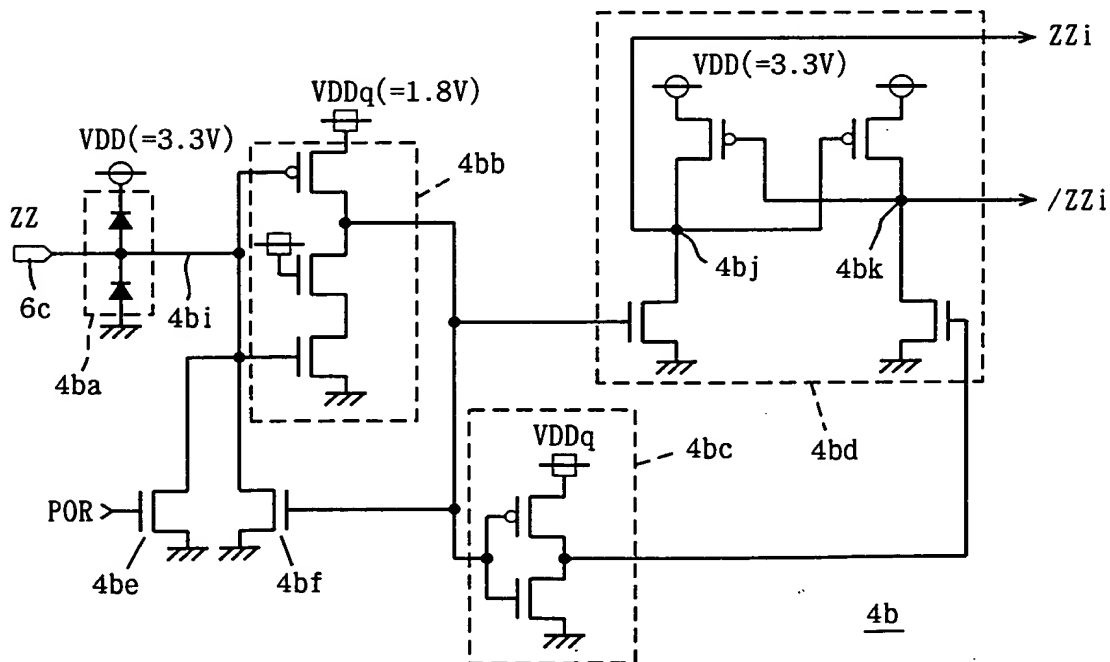


FIG. 7

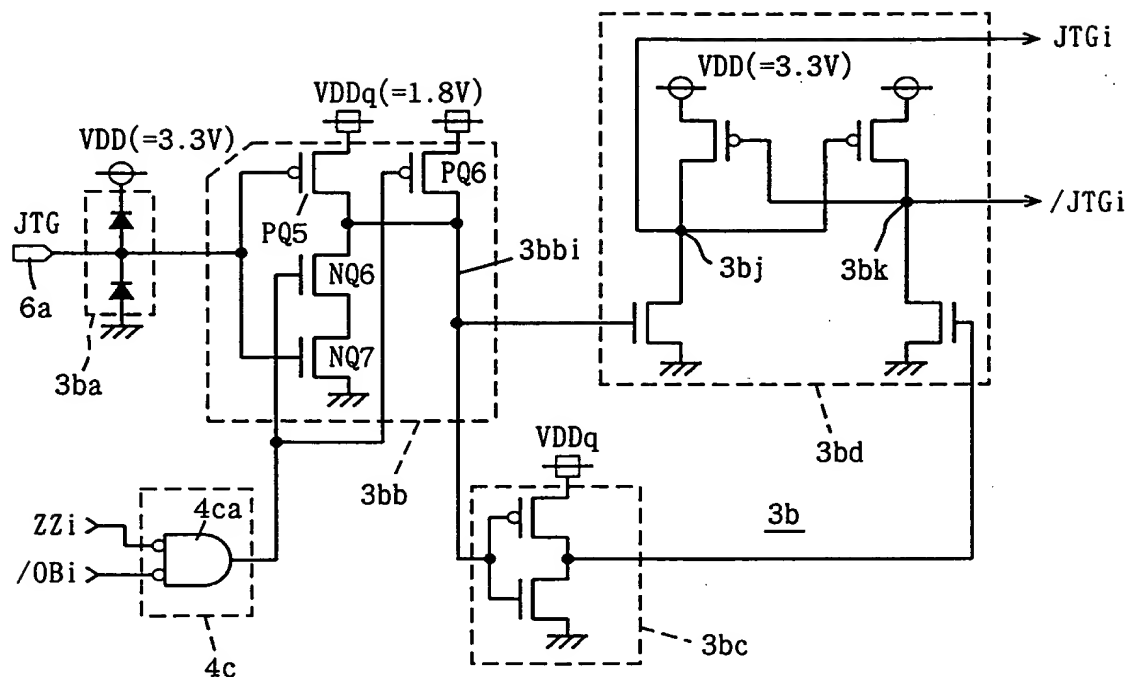


FIG. 8

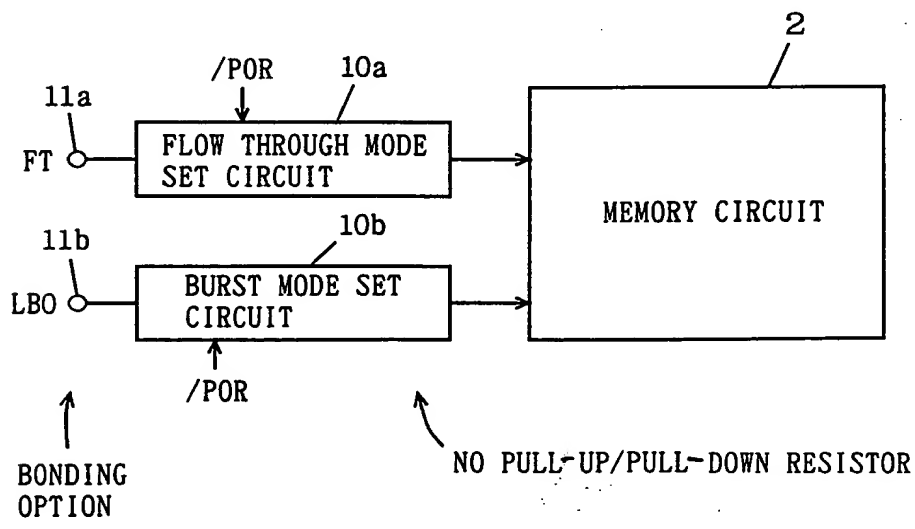


FIG. 9

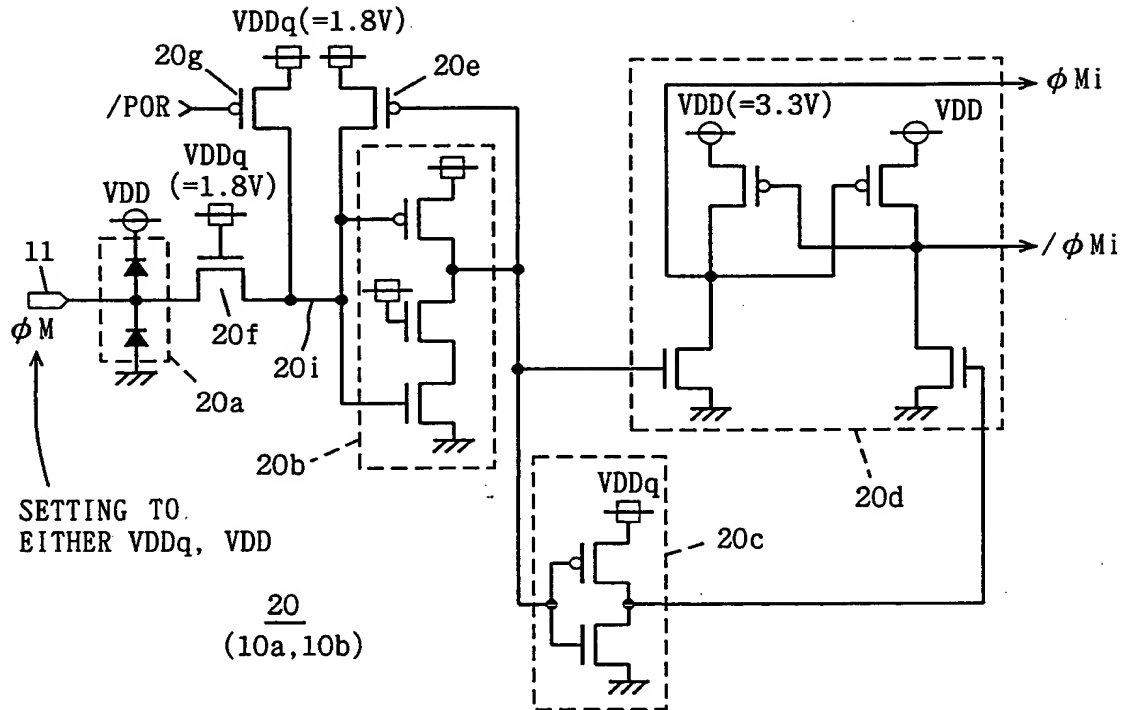


FIG. 10

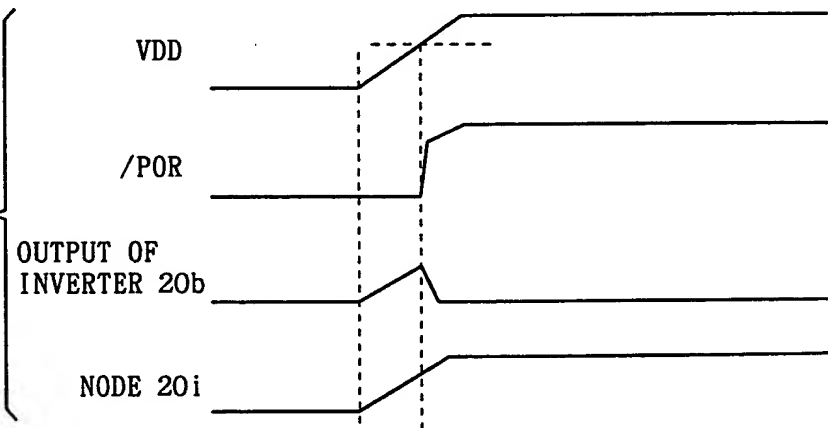


FIG. 11A PRIOR ART

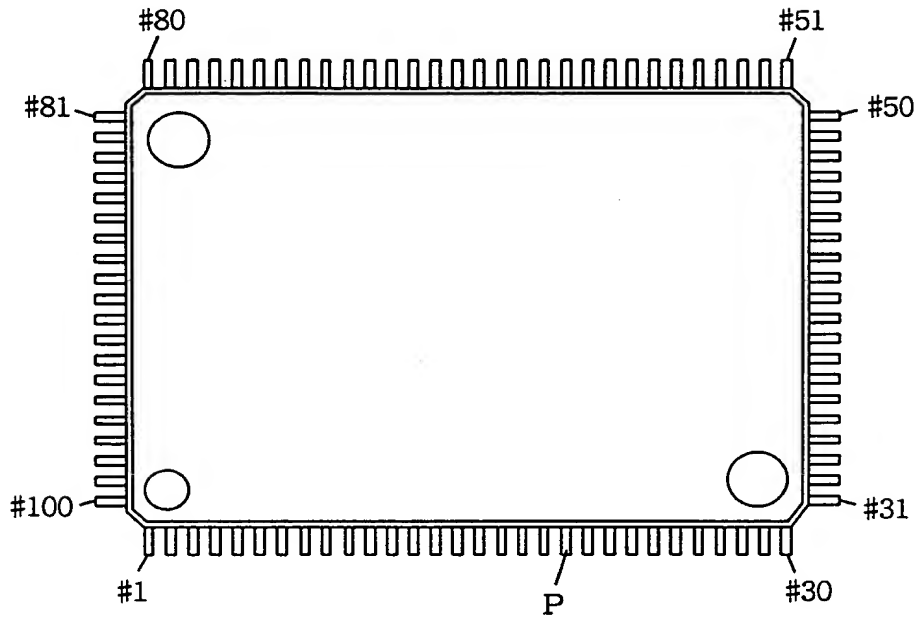


FIG. 11C
PRIOR ART

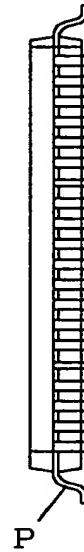


FIG. 11B PRIOR ART

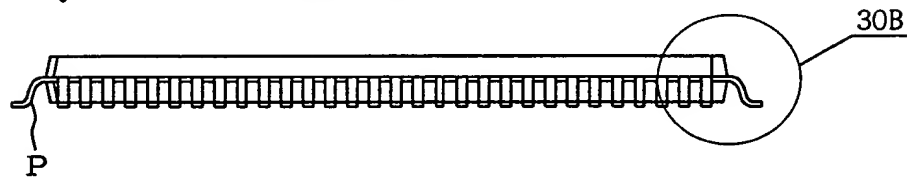
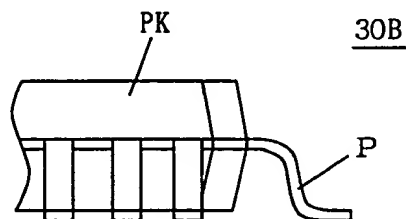


FIG. 11D PRIOR ART



SECRET 967560

APPROVED	O.G. FIG.	
BY	CLASS	SUBCLASS
DRAFTSMAN		

FIG. 12A PRIOR ART

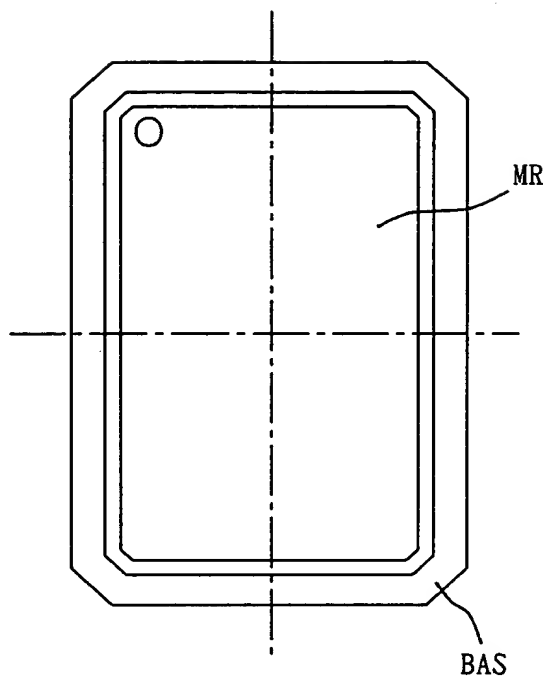


FIG. 12B PRIOR ART

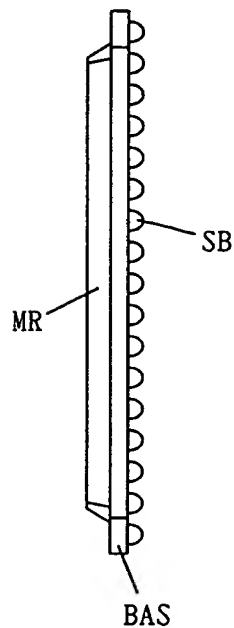
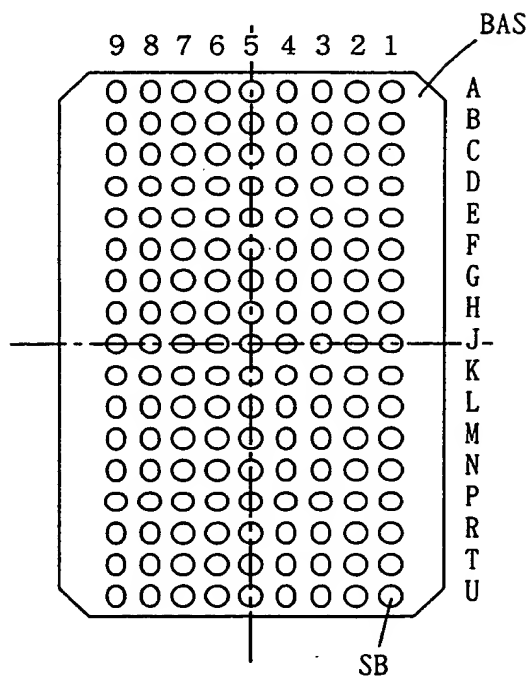
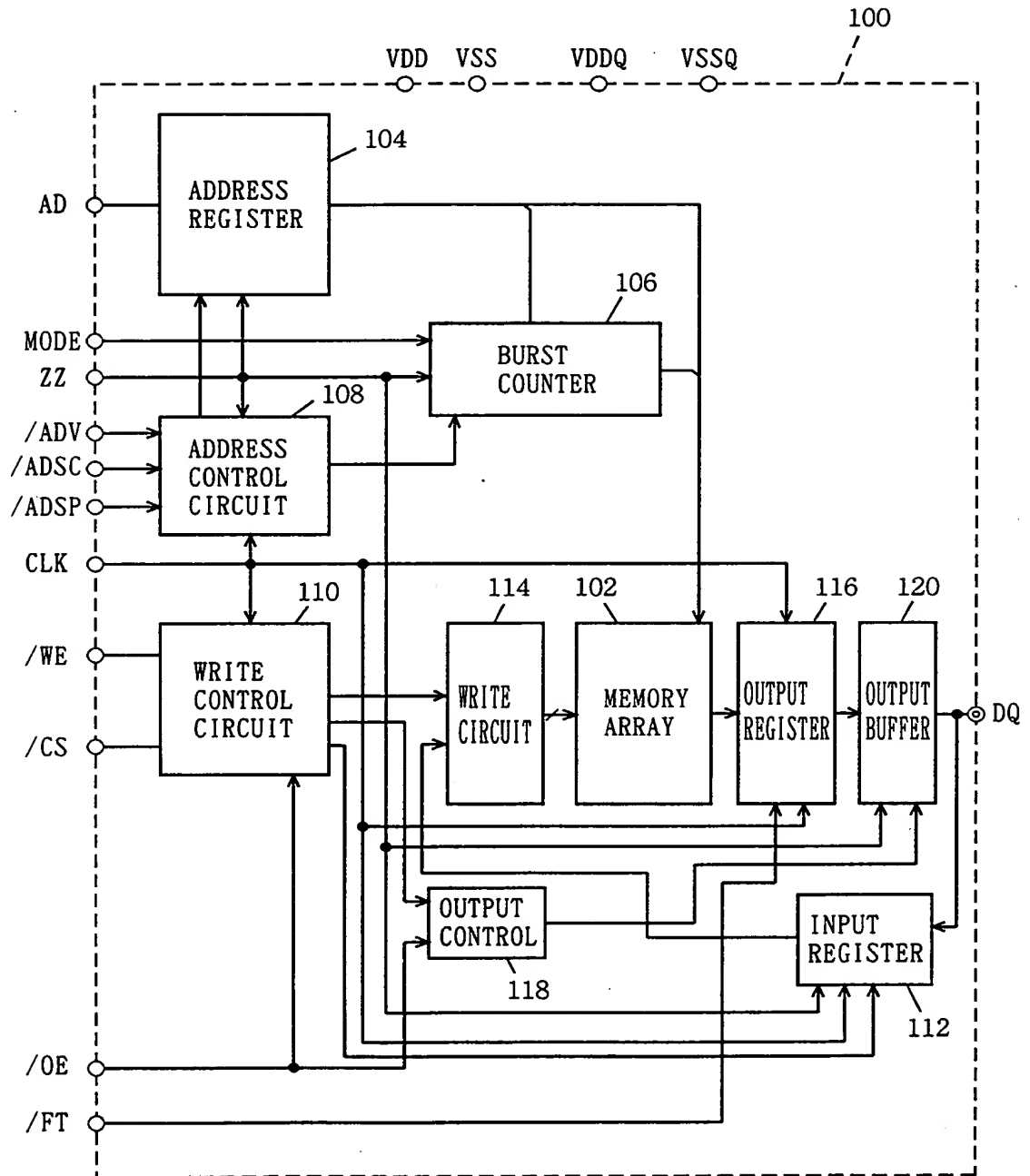


FIG. 12C PRIOR ART



2025 RELEASE

FIG. 13 PRIOR ART



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APPROVED	O.G. FIG.	
BY	CLASS	SUBCLASS
DRAFTSMAN		

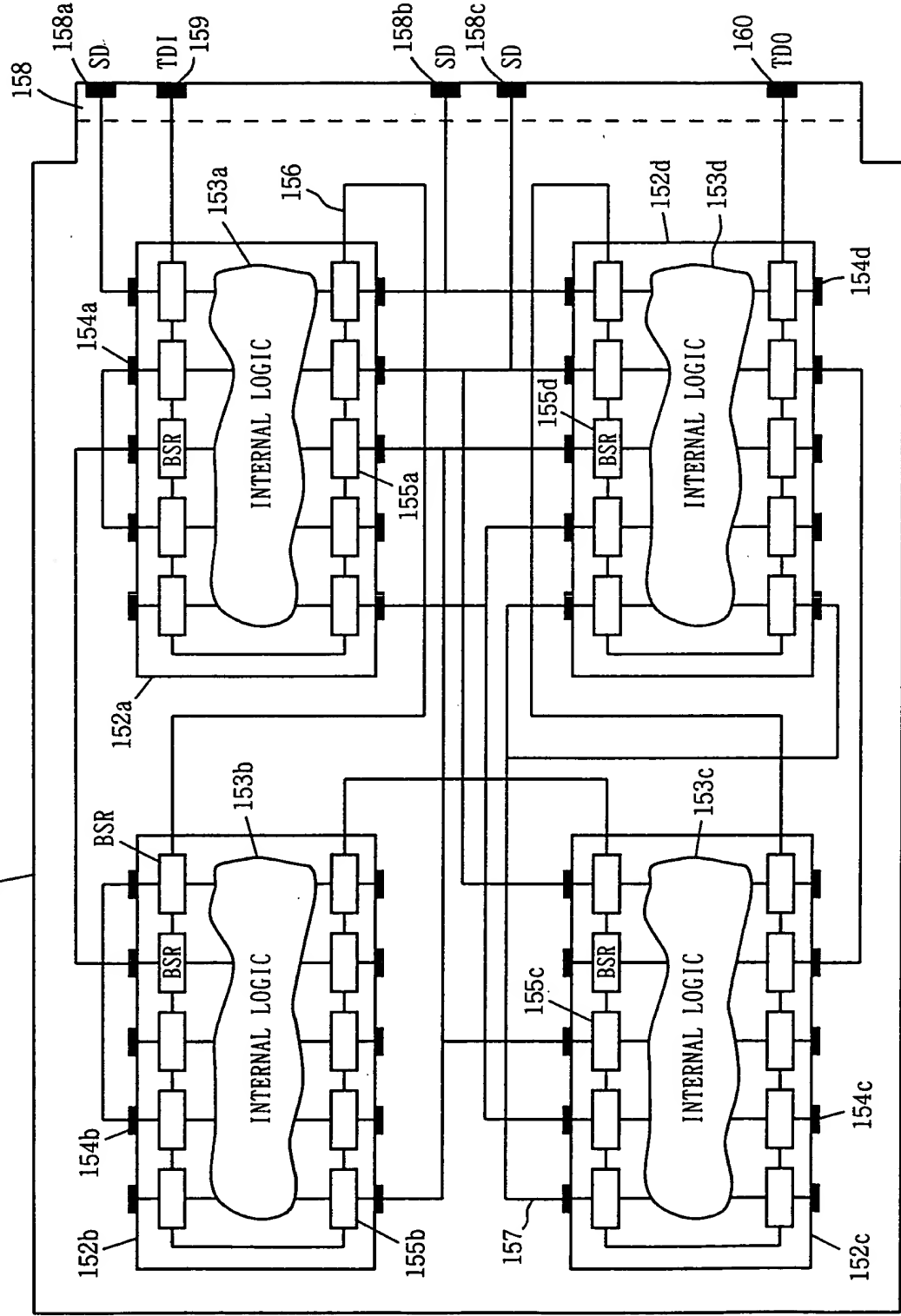
FIG. 15 PRIOR ART

	1	2	3	4	5	6	7
A	Vddq	SA	SA	SAP#	SA	SA	Vddq
B	NC	SE2	NC	SAC#	NC	SE3#	NC
C	NC	SA	SA	Vdd	SA	SA	NC
D	DQc	DQPc	Vss	NC	Vss	DQPb	DQb
E	DQc	DQc	Vss	SE1#	Vss	DQb	DQb
F	Vddq	DQc	Vss	G#	Vss	DQb	Vddq
G	DQc	DQc	SBWc#	SADV#	SBWb#	DQb	Vddq
H	DQc	DQc	Vss	SGW#	Vss	DQb	DQb
J	Vddq	Vdd	NC	Vdd	NC	Vdd	Vddq
K	DQd	DQd	Vss	CK	Vss	DQa	DQa
L	DQd	DQd	SBWd#	NC	SBWa#	DQa	DQa
M	Vddq	DQd	Vss	SBWE#	Vss	DQa	Vddq
N	DQd	DQd	Vss	SA1	Vss	DQa	DQa
P	DQd	DQPd	Vss	SA0	Vss	DQPa	DQa
R	NC	SA	LBO#	Vdd	FT#	SA	NC
T	NC	NC	SA	SA	SA	NC	ZZ
U	Vddq	TMS	TDI	TCK	TD0	TRST	Vddq

119 SOLDER BALLS (BUMP) ALLOCATED IN 32K·32
SYNCHRONOUS BURST SRAM

20040224 09:30:00

FIG. 16 PRIOR ART



360247-9 SEP 60

FIG. 17 PRIOR ART

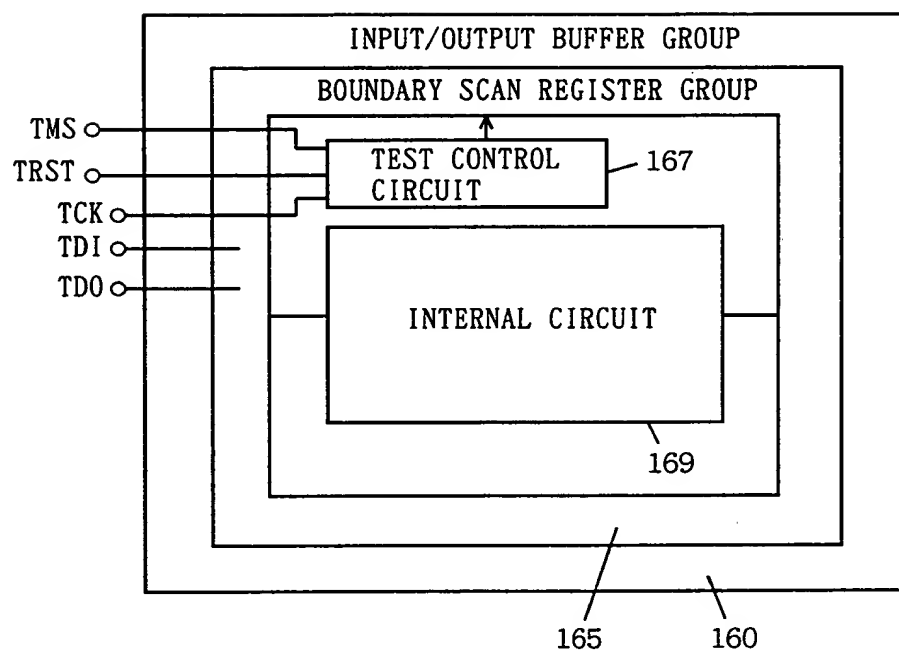


FIG. 18 PRIOR ART

SIGNAL \ STATE	NC	H	L
1FT(FT#)	NORMAL (PIPELINE)	NORMAL (PIPELINE)	FLOW THROUGH (NON-PIPELINE)
MODE(LB0#)	INTERLEAVE BURST	INTERLEAVE BURST	LINEAR BURST
ZZ	NORMAL	SNOOZE MODE	NORMAL

2025 RELEASE UNDER E.O. 14176

FIG. 19 PRIOR ART

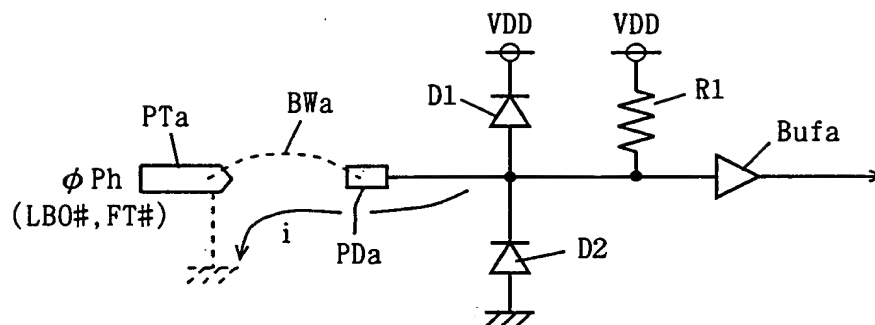


FIG. 20 PRIOR ART

